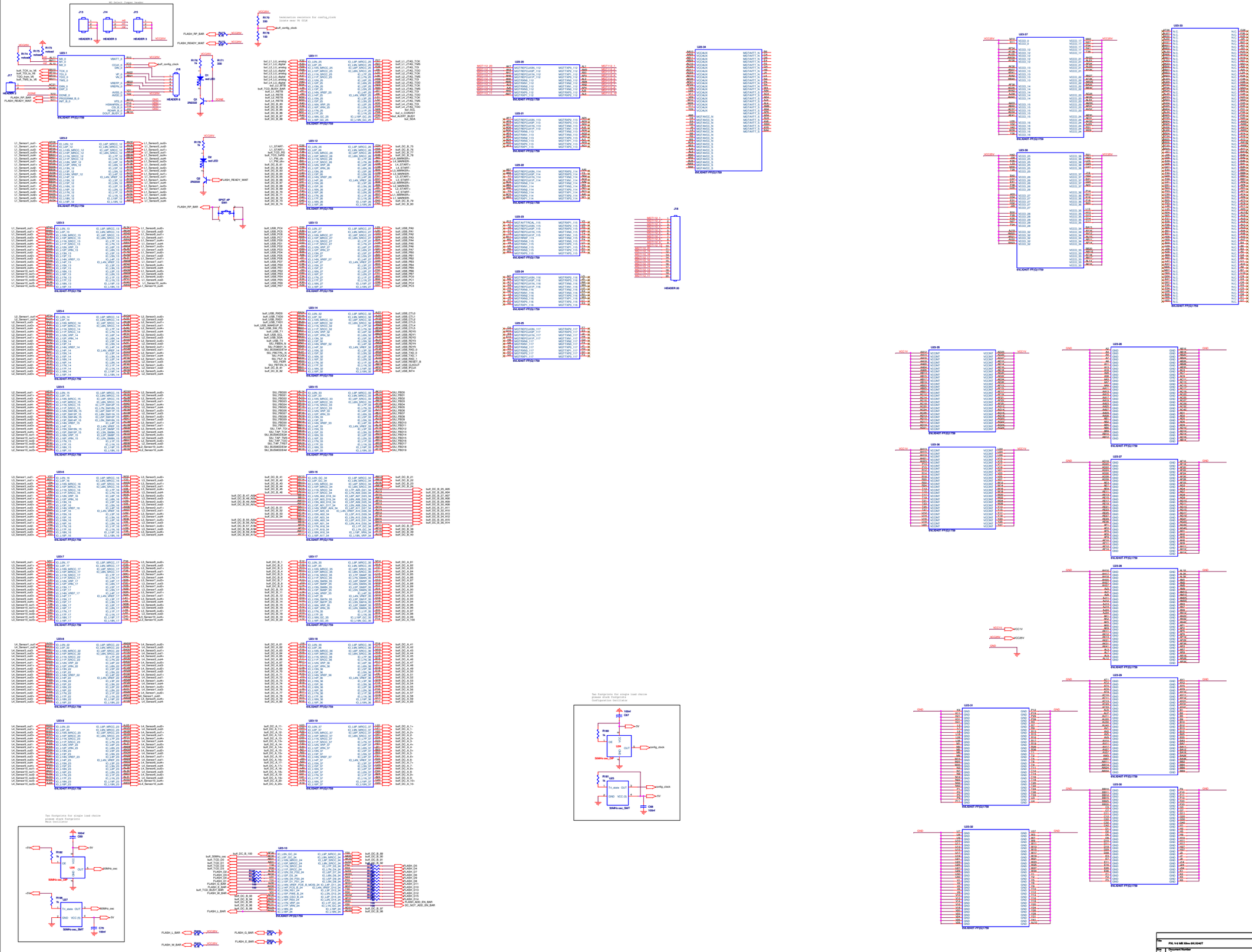
HS Debug Jumper header
HEADER 3
termination resistors for config_clock
locate near TK CCLK
buff_config_clock
FLASH_RP_BAR
FLASH_READY_WAIT
HEADER 6
FLASH_READY_WAIT
SPST-4P
SW1
FLASH_RP_BAR
U23-1
U23-2
U23-3
U23-4
U23-5
U23-6
U23-7
U23-8
U23-9
U23-10
U23-11
U23-12
U23-13
U23-14
U23-15
U23-16
U23-17
U23-18
U23-19
U23-20
U23-21
U23-22
U23-23
U23-24
U23-25
U23-26
U23-27
U23-28
U23-29
U23-30
U23-31
U23-32
U23-33
U23-34
U23-35
U23-36
U23-37
U23-38
HEADER 20
Two footprints for single load choice
please stack footprints
Configuration Oscillator
Two footprints for single load choice
please stack footprints
Anti-Resistance
FLASH_L_BAR
FLASH_W_BAR
FLASH_O_BAR
FLASH_E_BAR
File
PXL V-4 MB Xilinx BVLX240T
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Date
Wednesday, June 08, 2011
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