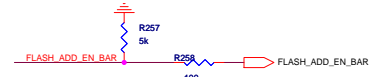
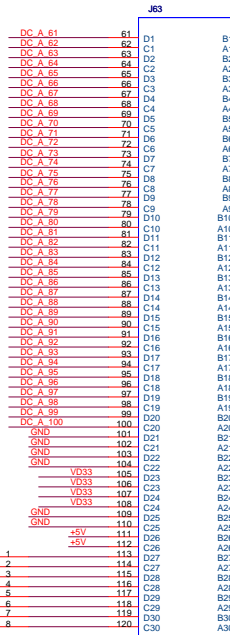
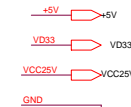
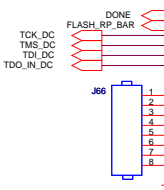
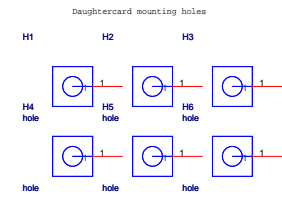
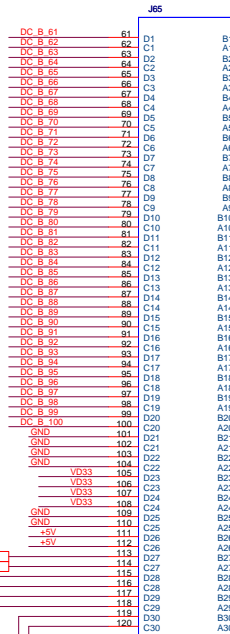


Daughter-card "A"



Daughter-card "B"



Notes:
Place buffer ICs near DC connectors