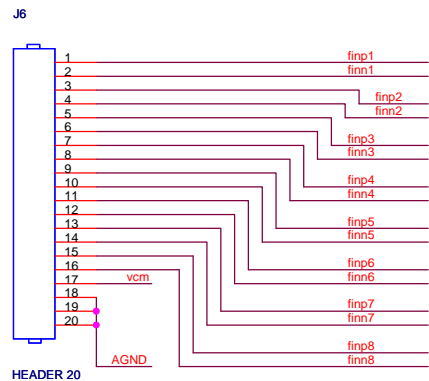
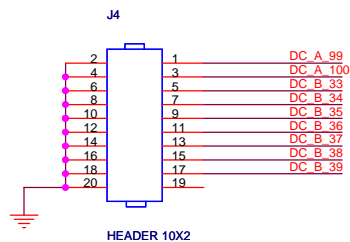
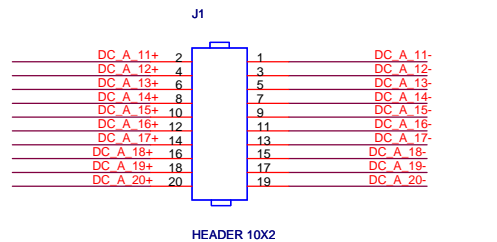
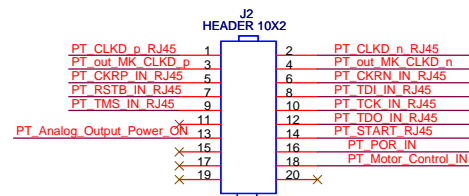


Daughtercard Spare IO Connectors

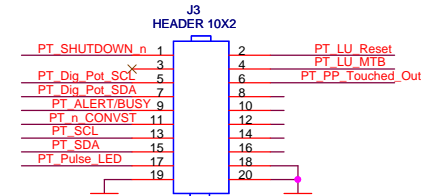


JTAG & CLOCK



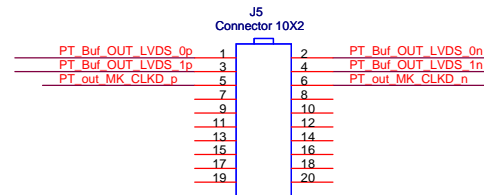
TO Spare IO input connector J269 on V5 Interface board
(Connected to V5 using Analog Switches Ron = 5ohms)

General Digial I/O

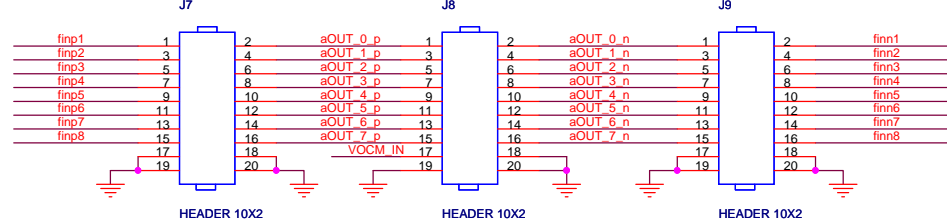


TO Spare IO input connector J268 on V5 Interface board
(Connected to V5 using Analog Switches Ron = 5ohms)

Data Buffer Out

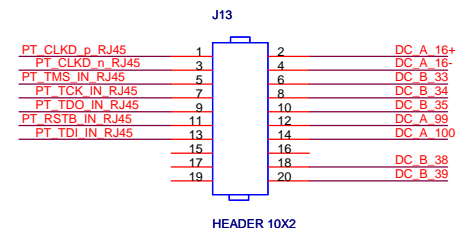
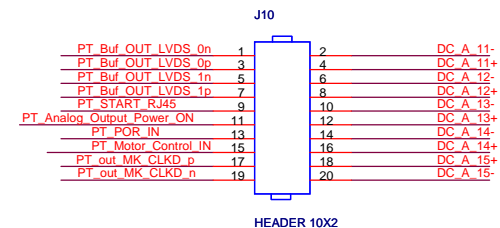
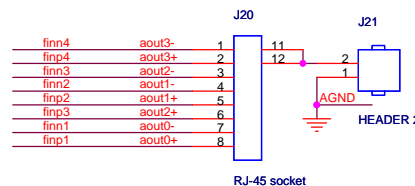
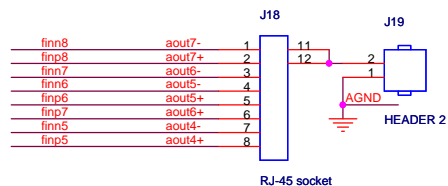
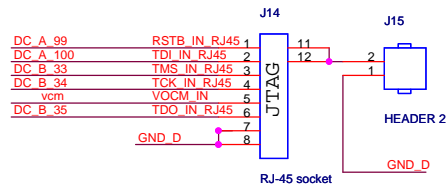
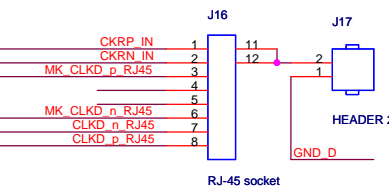
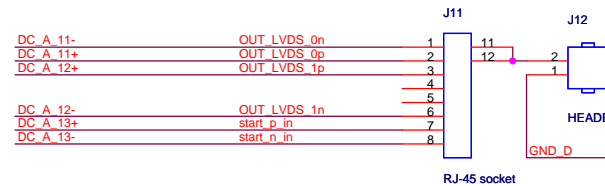


ANALOG BUFFER OUT



Jumpers to enable Ultimate Individual Test Board

Ultimate Individual Testing RJ-45 connectors



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|---|-----------------|--|------------|
| | | | |
| Title | | | |
| <Title> | | | |
| Size | Document Number | | Rev |
| C | <Doc> | | <Rev Code> |
| Date: Saturday, March 10, 2012 Sheet 1 of 4 | | | |