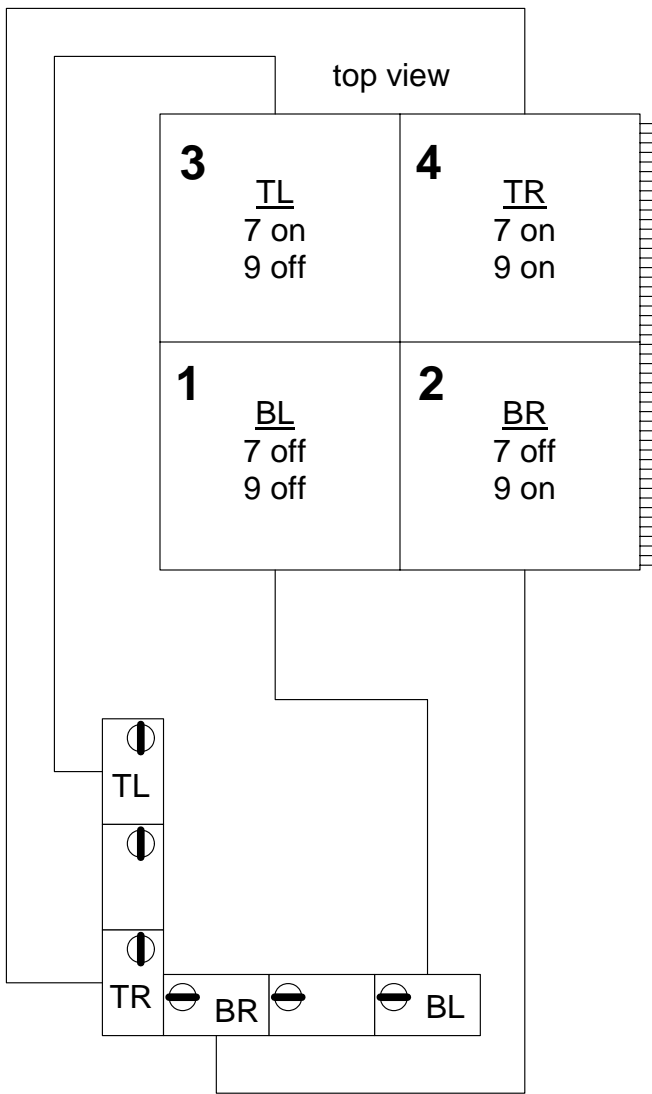
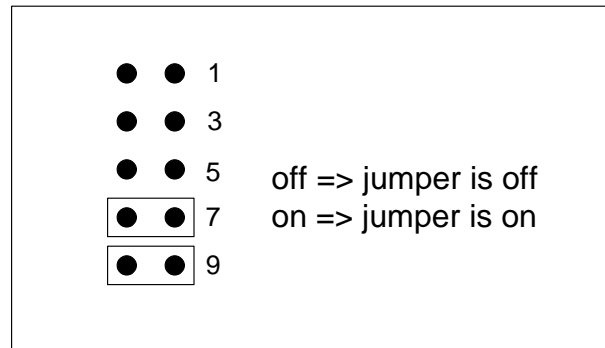


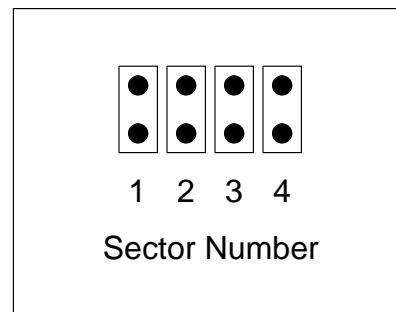
MIMOSA5 Sectors Location, Reference Voltage, and Jumper Adjustment



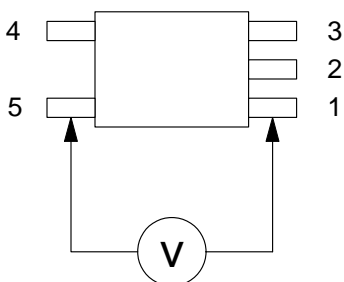
512 x 512 FPGA code



512 x 128 FPGA code



Measure Vref on the driver located adjacent to the potentiometer.



CW increases Vref
CCW decreases Vref
0.1 V ~ 1/2 rotation