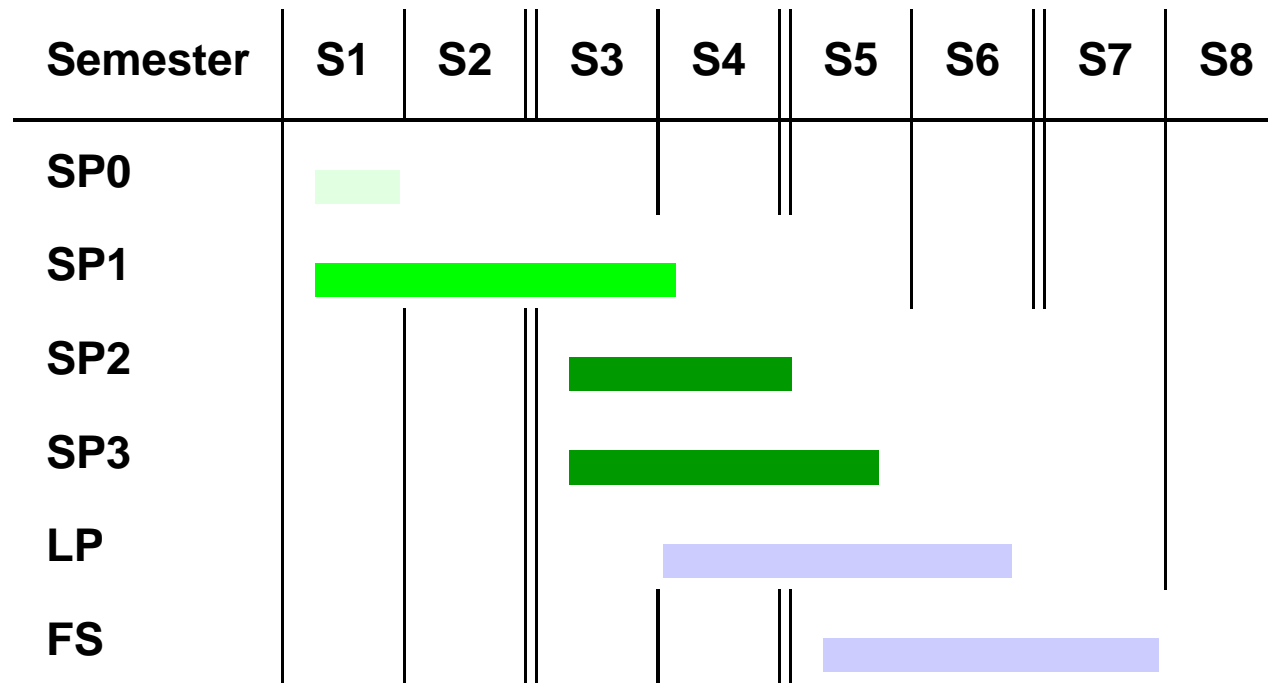


- **EUDET like sensors** \rightarrow **continuous read-out (M-16 like architecture)** \rightarrow **power dissip., pile-up ?** :
 - ⊕ MIMOSA-22 (= M-16 with **JTAG, 18 μm pitch, 64 columns of 1 cm, testability, ...**) : subm. **end Sept. '07**
 - ⊕ MIMOSA-22+ (= M-22 with \emptyset , **4x64 columns, optim. pixels, testability, ...**) : subm. **June '08**
 - ⊕ Final sensor (= M-22+ fine tuned with **1088 columns**) : subm. **end '08/early '09**
 - ⊕ STAR final sensor (= EUDET sensor with \sim **twice number of rows**) : subm. \gtrsim **Summer '09**

- **2-capacitor pixels with decoupled r.o.** (\sim **M-16 modified for trigger, $t_{int} \lesssim 25 \mu s$**) \rightarrow **dead time, time-line** :
 - ⊕ 1st prototype (**analog output, variants of pixel architecture, pitch ?, r.o. architectures**) : subm. **early '08**
 - ⊕ 2nd prototype (**digital output, 64 - 256 col. r.o. in //, CDS in pixel, various pixel arrays, discr. output, \emptyset ? \rightarrow M-22+**) : subm. **Jan. '09**
 - ⊕ 3rd prototype ??? \rightarrow **EUDET sensor ???**
 (= 2nd proto. with \emptyset , **optim. pixel, $\gtrsim 256$ col., ...**) : subm. \geq **Autumn '09**
 - ⊕ Final sensor (= 3rd proto. with **full 2x2 cm² surface**) : subm. **2nd half of 2010 !!**

- **Low profile (fast converging) alternative** : **sensors with analog output & smallest possible pitch ?**
 \rightarrow **data flow, integration time, pile-up, rad. tolerance (pitch ?)** :
 - ⊕ \sim **Natural continuation of MIMO★-3**
 - ⊕ **Final chip in 2009 ?**



- Sensor production based on 5 steps (perhaps only 4, i.e. SP3 included in LP) :
 - ⊕ MIMOSA-8 ≡ SP-0 : 25 μm pitch, epi < 7 μm
 - ⊕ MIMOSA-16 ≡ SP-1 : 25 μm pitch, epi \sim 11 or 15 μm , rad. tol., enhanced ampli.
 - ⊕ SUZE-01 ≡ SP-2 : \emptyset μ circuit with integrated output memories
 - ⊕ MIMOSA-22 ≡ SP-3 : like SP-1 but 18 μm pitch, optimised pixels, 64+8 col. of \sim 500 pixels
 - ⊕ M22+ ≡ LP : like SP-3 but \geq 256 col. of \sim 550 pixels and integ. \emptyset
 - ⊕ M22++ ≡ FS : like LP but \sim 1100 col. of \sim 550 pixels