Gain = 0.5 x Rf/Rg = 2 (Could be changed to smaller by reduce Rf)

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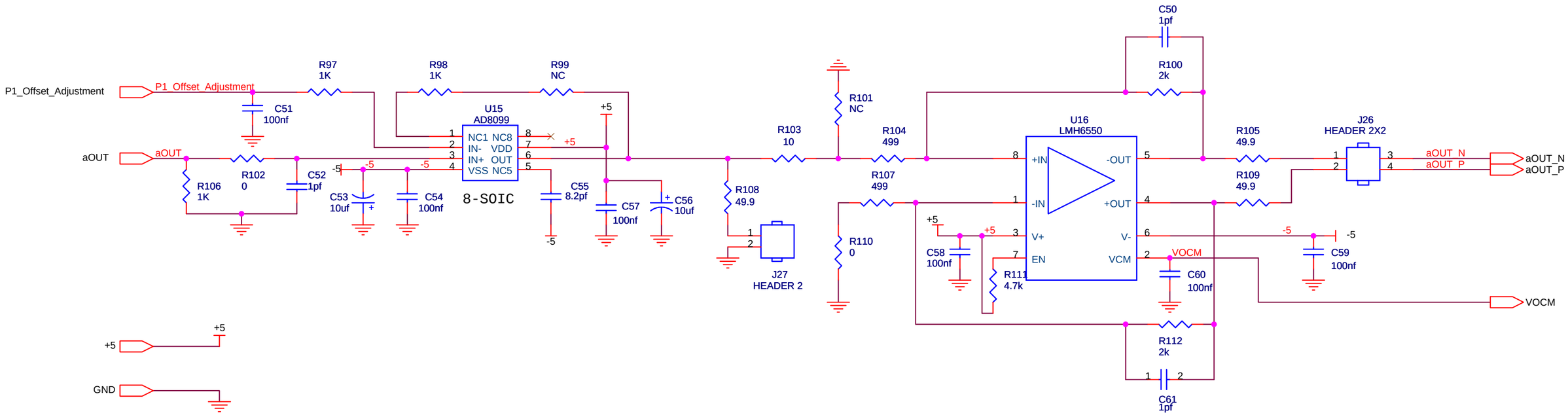

Title	**Phase 1 Analog Output Buffer**	
Size B	Document Number <Doc>	Rev 0
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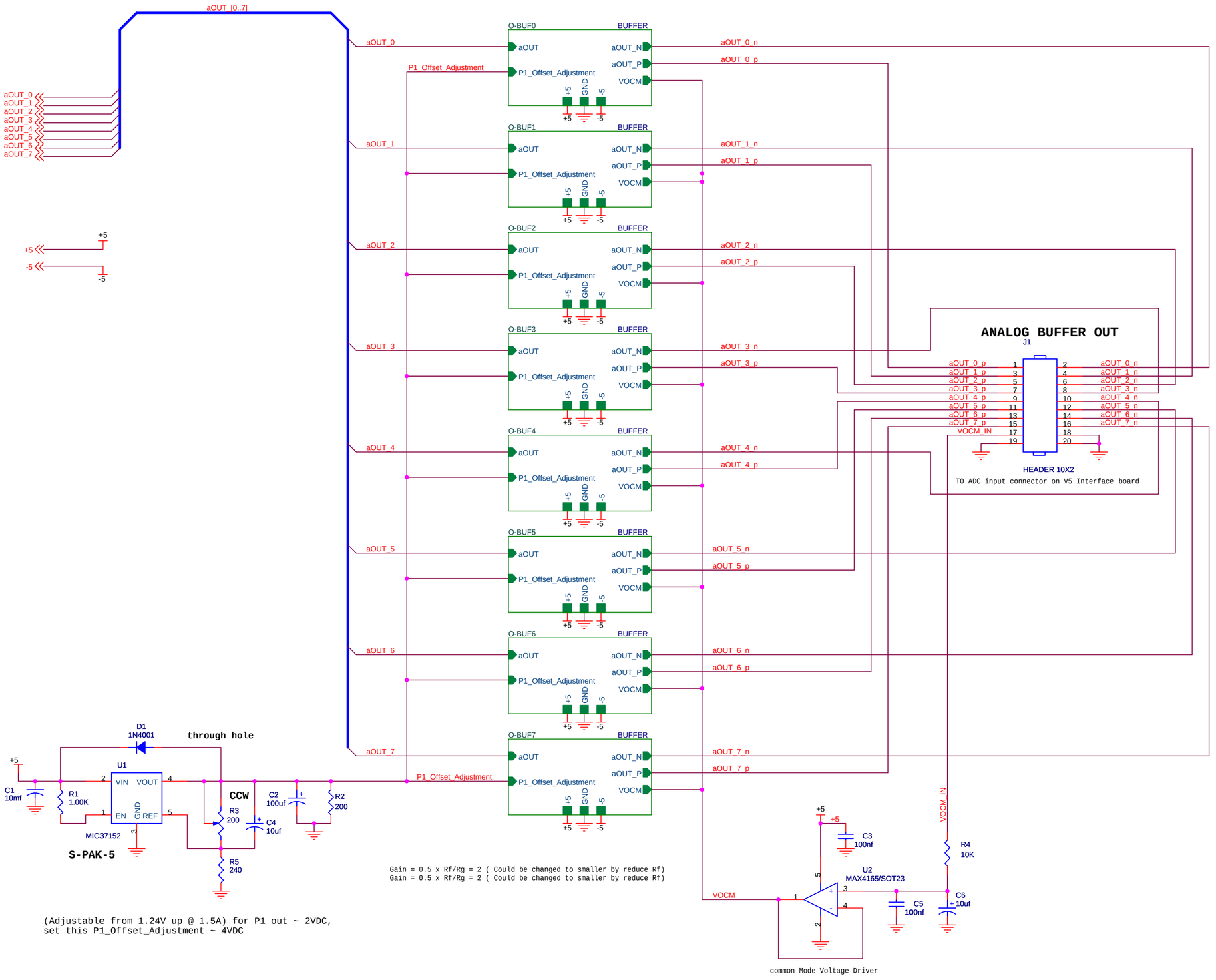
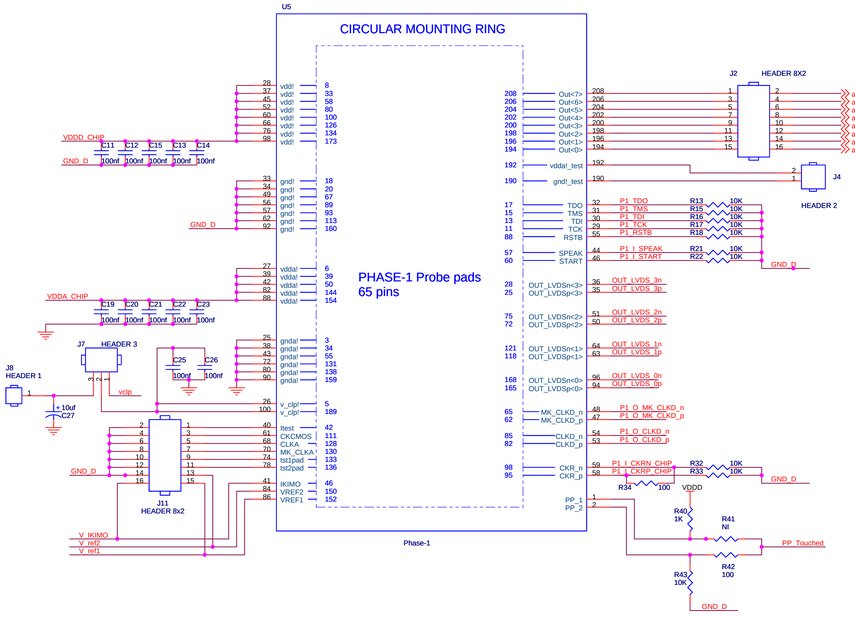


Title	**Phase 1 Analog Output Buffer**	
Size C	Document Number <Doc>	Rev 0
Date: Thursday, July 09, 2009	Sheet 1 of 10	



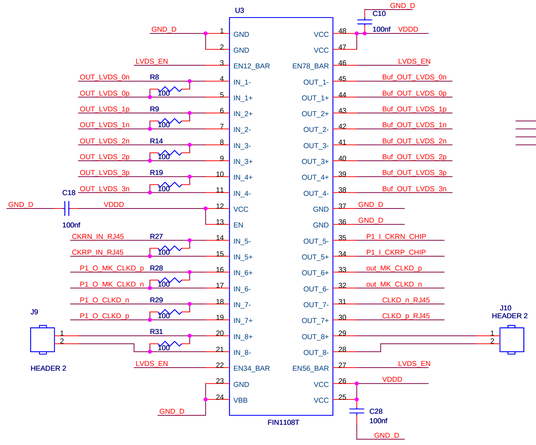
PROBE CARD NOTES:

1) All the Bypass cap need to locate as close as possible to the probe pins

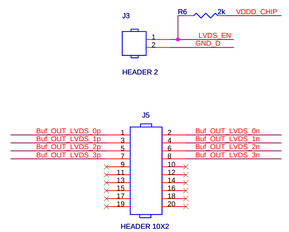
2) All the Bypass cap (except tantalum) and resitor are SM 0603 except where noted

3) All differential pairs are 100 ohms impedance traces

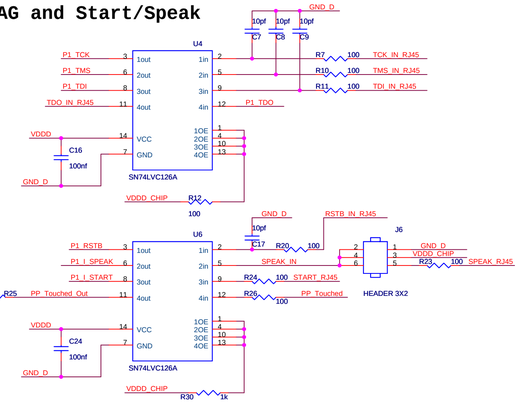
CLOCK IN/OUT & PHASE 1 LVDS data out



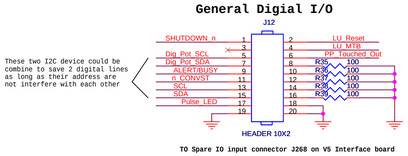
V5 DIGITAL INTERFACE



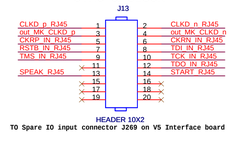
JTAG and Start/Speak



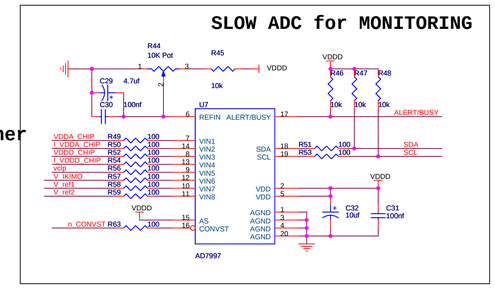
General Digial I/O



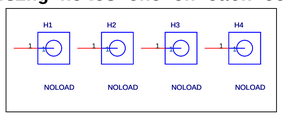
JTAG & CLOCK



SLOW ADC for MONITORING

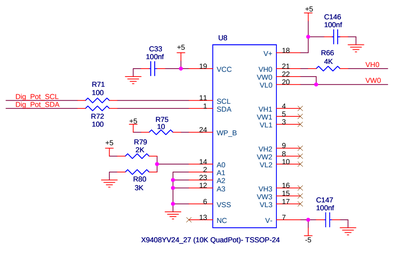


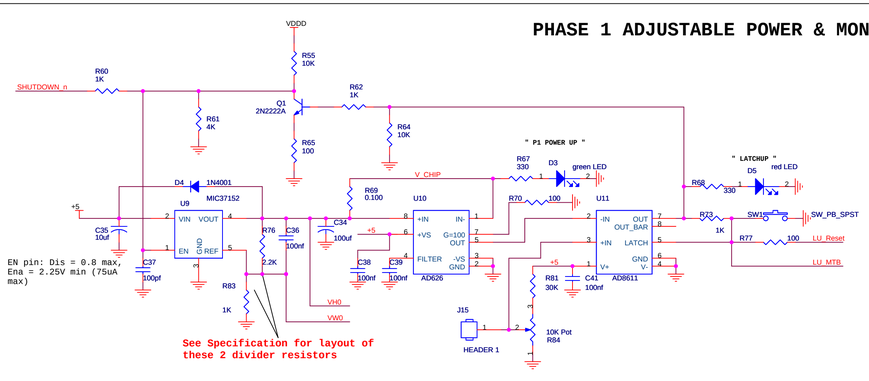
Mounting holes one on each corner

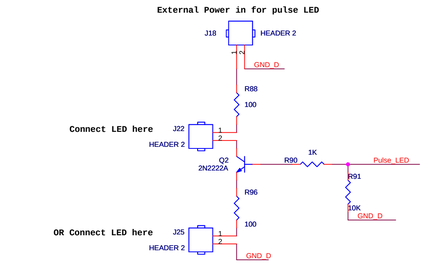


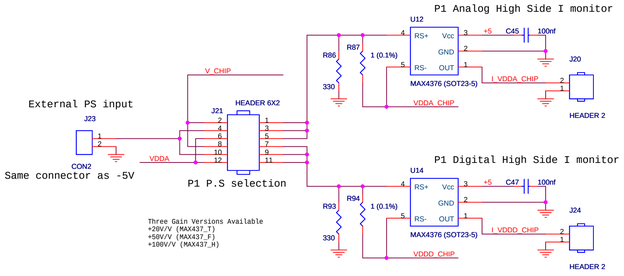
PHASE 1 ADJUSTABLE POWER & MONITOR

(2500 ohms/64 = 39 ohms/step)



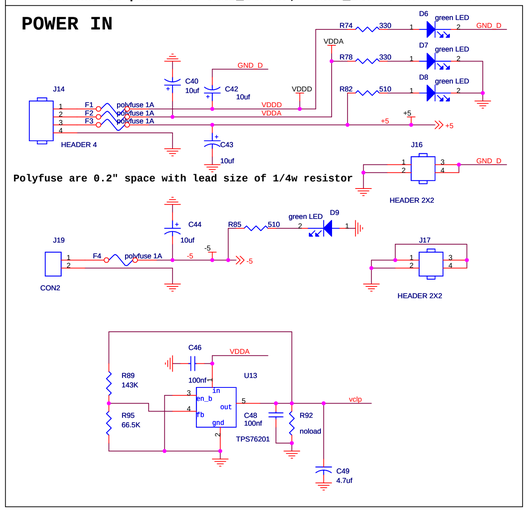






VddD_CHIP & VddA_CHIP are for the Phase_1 IC ONLY, Other IC power use VddD_Buffer, VddA_Buffer or +5V/-5V

POWER IN



Title	PIXEL - Phase 1- Probe Card	
Size D	Document Number <Doc>	Rev A
Date: Thursday, July 09, 2009	Sheet 2 of 10	